

UNITED STATES DEPARTMENT OF COMMER $oldsymbol{arphi}$

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Washington, D.C. 20231

APPLICATION NO.	FILING DATE FIRST NAMED INV		VENTOR		ATTORNEY DOCKET NO.	
09/659,885	09/12/00	MENEGOLI		p	850063.498D2	
		•	7	EXAMINER		
E RUSSELL T	ARLETON	MM91/1024		LOKE S		
SEED INTELLECTUAL PROPERTY LAW GROUP				ART UNIT	PAPER NUMBER	
701 FIFTH A	VENUE	•		,		
SUITE 6300				2811		
SEATTLE WA	98104-7092	•		DATE MAILED:	•	

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

10/24/01

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MENEGOLI, PAOLO						
address						
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
4) Claim(s) 17-20 is/are pending in the application.						
4a) Of the above claim(s) is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>17-20</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or election requirement.						
Application Papers						
9) The specification is objected to by the Examiner.						
10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.						
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
niner.						
If approved, corrected drawings are required in reply to this Office action.						
12) The oath or declaration is objected to by the Examiner.						
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).						
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents have been received.						
2. Certified copies of the priority documents have been received in Application No						
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the partified enrice not received.						
* See the attached detailed Office action for a list of the certified copies not received.						
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application). a) ☐ The translation of the foreign language provisional application has been received.						
15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121. Attachment(s)						
:						
No(s) PTO-152)						
the merits is all Stage all application).						

Application/Control Number: 09/659,885

. Art Unit: 2811

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 17, 19 and 20 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Cogan (U.S. Patent no. 4,811,065 in PTO-1449).

In regards to claim 17, Cogan shows all the elements of the claimed invention in figs. 6 and 7 and in column 4, lines 25-56 of the written description. It comprises: a method of operating a DMOS transistor [90] comprising diverting current from a source [92] of the DMOS transistor with a Schottky diode [110, 102] that is co-integrated with the DMOS transistor when the source [92] becomes more positive than a drain [100] of the DMOS transistor.

In regards to claim 19, Cogan shows the act of diverting current from a source of the DMOS transistor with a Schottky diode includes diverting current from a p-n junction body diode [91, 102] having a cathode coupled to the drain [100] and an anode coupled to the source [92].

In regards to claim 20, Cogan shows the act of diverting current from a source of the DMOS transistor with a Schottky diode includes diverting current from the source with a Schottky diode having a cathode coupled to the drain [100] and an anode coupled to the source [92].

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

Application/Control Number: 09/659,885

. Art Unit: 2811

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 18 is rejected under 35 U.S.C. 103(a) as being unpatentable over Cogan in view of Pearce (EP 0747969 A1 in PTO-1449).

Cogan differs from the claimed invention by not showing the act of diverting current from a source of the DMOS transistor includes diverting current from a parasitic bipolar transistor having a collector coupled to a substrate in which both the DMOS transistor and the Schottky diode integrated.

Pearce shows an n-channel type lateral DMOS transistor formed on a p-type substrate in fig. 2.

Since both Cogan and Pearce teach an n-channel type DMOS transistor with a lateral channel region, it would have been obvious to have the p-type supporting substrate of Pearce in Cogan because it is a conventional supporting substrate for DMOS transistor.

The combined device of Cogan and Pearce would show the act of diverting current from a source of the DMOS transistor includes diverting current from a parasitic bipolar transistor having a collector coupled to a substrate in which both the DMOS transistor and the Schottky diode integrated.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Loke whose telephone number is (703) 308-4920. The examiner can normally be reached on 7:45 am to 5:15 pm.

. Art Unit: 2811

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (703) 308-2772. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7722 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

sl October 19, 2001 Steven Loke Primary Examiner